

FIG. 2

PRIOR ART



FIG. 3

PRIOR ART

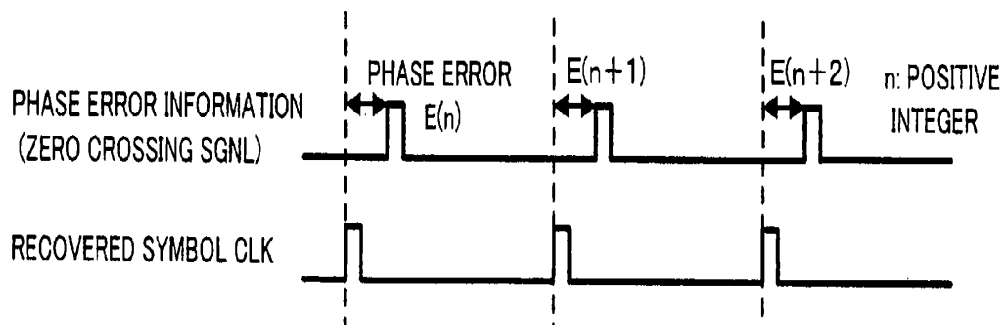


FIG. 4
PRIOR ART

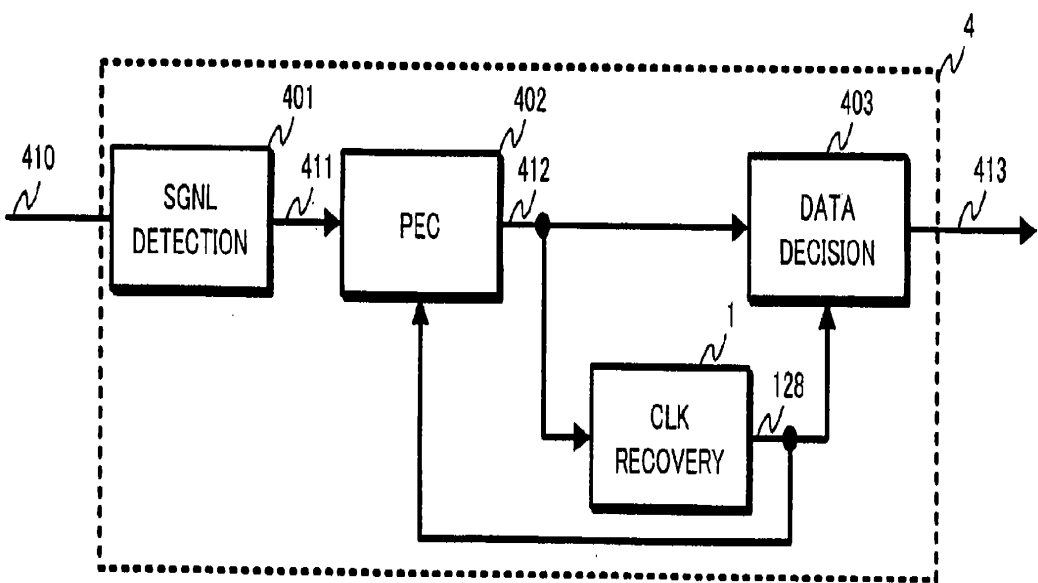


FIG. 5

PRIOR ART

BIT $X_n X_{n+1}$	PHASE TRANSITION
1 1	$-3\pi/4$
0 1	$3\pi/4$
0 0	$\pi/4$
1 0	$-\pi/4$

FIG. 6

PRIOR ART

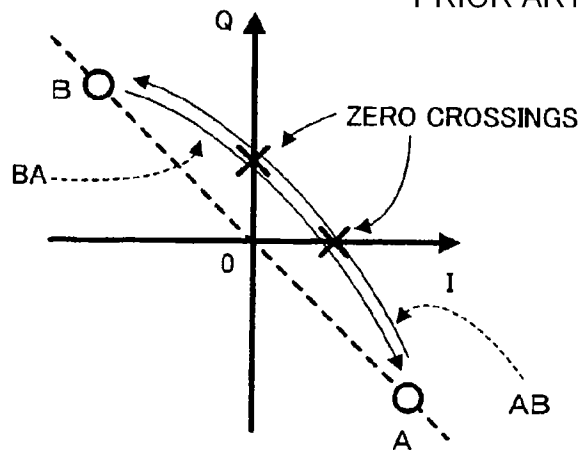
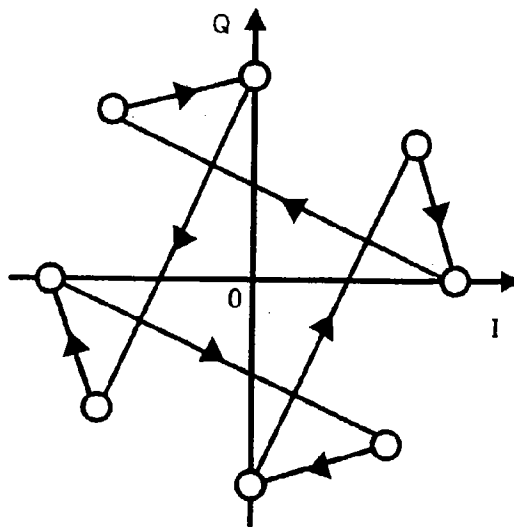


FIG. 7

PRIOR ART



REPLACEMENT SHEET

FIG. 8

PRIOR ART

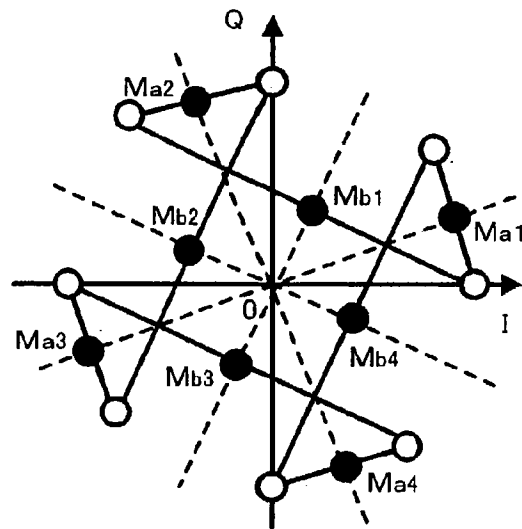


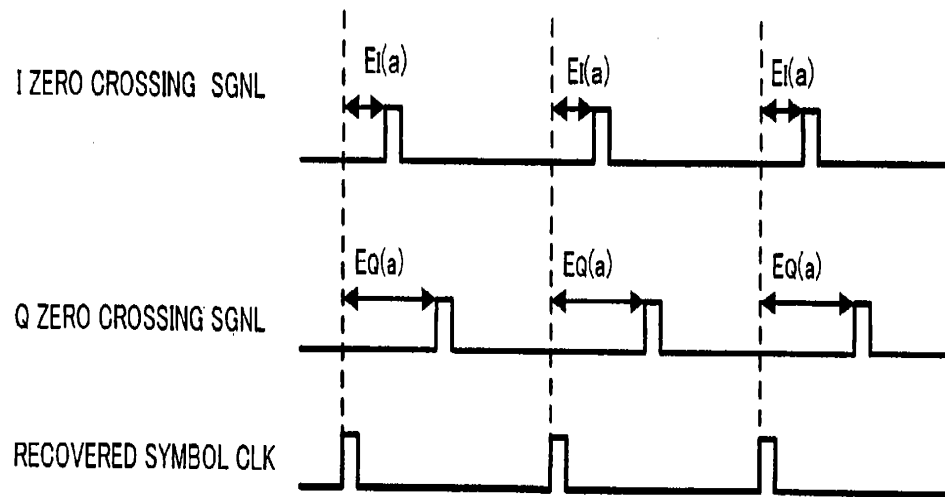
FIG. 9
PRIOR ART

FIG. 10

PRIOR ART

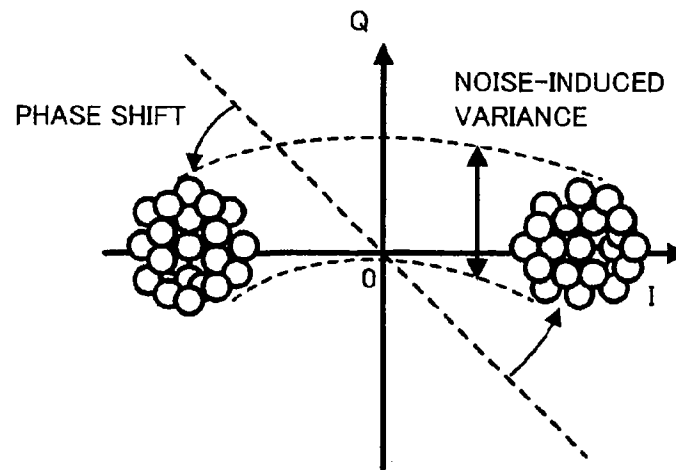


FIG. 11

PRIOR ART

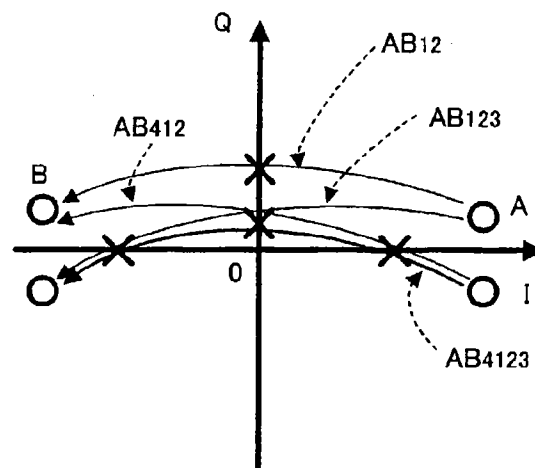


FIG.12

PRIOR ART

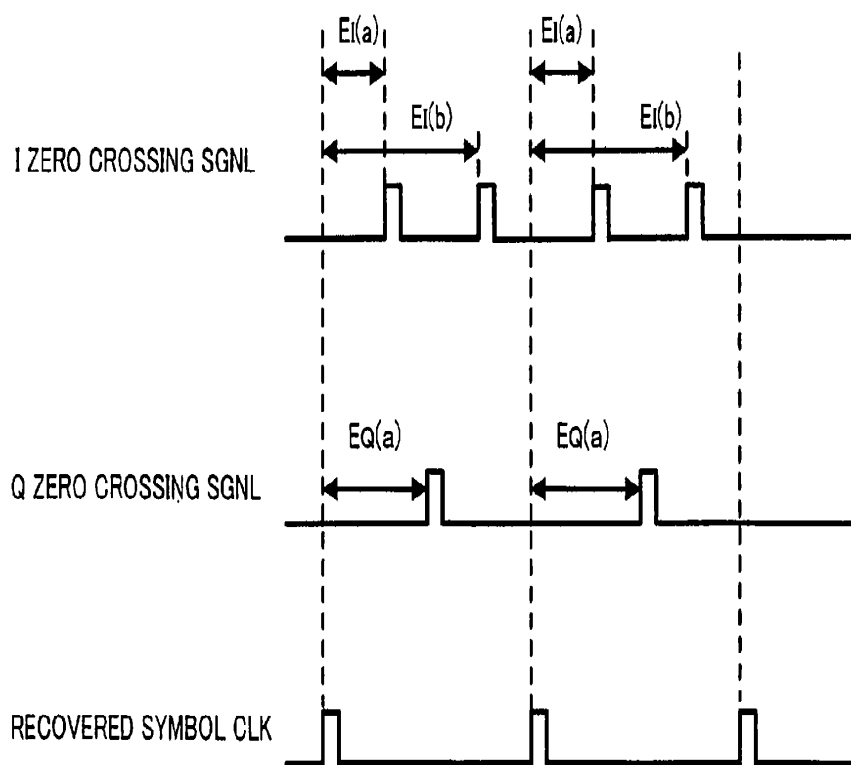


FIG.37

PRIOR ART

